BLIP TESTING PROCEDURE Q BIAS: BOARD #	CDMS STATUS_	
Q INNER BIAS SECTION:		
 Test connections and set up: Measure QI Bias at QI Bias Mon. and and at the Aux. output of the Breakon Use a DMM and oscilloscope to mean 	ut Box(BOB).	-
2. Power up settings:		QIDAC OUT: 10V QI I MON: 1(off) GRND Q INNER: 1(on)
3. Test preparation: Readfile qbias.macro Set DAC to 1V: QIDAC(1) Turn off current mon.: QiImon(0) Unground QI: gndQI(0) Select Mux 1 output: Mux1(QiVmo	n)	QIDAC OUT: 1 V QI I MON: 1(off) GRND Q INNER: 0(off)
 4. QI Bias Test: Input settings in Step 3 before star Set QIDAC to different values from -10V to +10V and record measurements QIDAC(x) No. 	eting test. Solution: $x = -10$ to $\frac{10}{2}$	QIDAC OUT: -10 to +10V QI I MON: 1(off) GRND Q INNER: 0(off)
Is the output linear? Does front panel readout ma breakout box reading within	tch	DAC BOB Front Panel Mux 1 Mux 2 10 5 2 0
	-	5 10
5. Ground Bias Test: Input settings in Step 3 before starting Ground QI: gndQI	g test.	QIDAC OUT: 1 V QI I MON: 1(off) GRND Q INNER: 1(on)
Does output go to Zero?		
6. QiImon test: Input settings in Step 3 before starting	g test.	QIDAC OUT: 1 V QI I MON: 0(on) GRND Q INNER: 0(off)
~	mon(0) x1(QiImon).	

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ry DAC settings and monitor Mux. 1 for chechanges occur?	anges.	
e the changes linear?		
	-	
OUTTER BIAS SECTION:		
Test connections and set up:		
Measure QO Bias at QO Bias Mon. and M	lux. 2 output	connectors on the front pane
and at Aux. output of the Breakout Box(Bo		
Use a DMM and an oscilloscope to measur	re the output	•
Power up settings:	Γ	OODAC OUT. 10V
Tower up settings.		QODAC OUT: 10V QO I MON: 1(off)
		GRND Q OUTTER: 1(on)
		Old VD Q OUT TEXt. T(oil)
Test preparation:	Г	
16.1		QODAC OUT: 1 V
readfile qbias.macro et DAC to 1V: QODAC(1)		QO I MON: 1(off)
QODAC(1) urn off current mon.: $QOImon(1)$		GRND Q OUTTER: 0(off)
and on eartene monitive $QOImon(1)$ and $QO(0)$		
elect Mux 2 output: Mux2(QoVmon)		
	_	
QO Bias Test:		QODAC OUT: -10 to +10
Input settings in Step 3 before starting to		QO I MON: 1(off)
		GRND Q OUTTER: 0(off)
et QODAC to different values from -10V to -	- +10V and re	cord measurements
QODAC(x) Note: x		
	DA	C BOB Front Panel Mu
s the output linear?	10 5	
and from t remail mandage match	$\frac{3}{2}$	
bes front panel readout match breakout box reading within	0	
0.1V?	-2 -5	
	-10	
	<u> </u>	
Ground Bias Test:		QODAC OUT: 1 V
put settings in Step 3 before starting test.		QO I MON: 1(off)
cound OO:		GRND Q OUTTER: 1(on)
ound QO: gndQO pes output go to Zero?	L	
	Γ	QODAC OUT: 1 V
		QODAC OUT. TV QO I MON: 0(on)
		GRND Q OUTTER: 0(off)
		CIAID Q OUT ILK. U(UII)

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6. QoImon test: Input settings in Step 3 before st	tarting test.	
Turn on current monitoring: Set Mux. 2 to monitor current:	QoImon(0) Mux2(QoImon).	
Vary DAC settings and monitor M	Iux. 2 for changes.	
Do changes occur?		
Are the changes linear?	•	